

QUICK REFERENCE

JUMPER SETTINGS (* : Initial Setting)

W1 - Power Detection Circuitry		
Power Failure Detection *	1-2	
Low Battery Detection	2-3	

W2 - Battery		
Connected		on
Disconnected *		off

Danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type. Dispose of used batteries according to the manufacturer's instructions.

W3 - HD Interface		
Disabled *		off
To IDE Interface		on

W4 - Watchdog		
Disabled		off
Enabled *		on

W5 - Power Monitoring		
Enabled		on
Disabled *		off

W6 - Flash EPROM		
Installed *		on
No Flash		off

W7 - BIOS Boot		
EPROM BIOS *		off
Flash BIOS		on

W8 - TEK BIOS Exten.		
Enabled *		off
Disabled		on

W9, 10, 11, 12, 13, 14 - Serial Port 2 Mode						
	W9	W10	W11	W12	W13	W14
RS-232 *	1-2	1-2	off	off	1-2	1-2
RS-485	2-3	2-3	on	on	2-3	2-3

W14A - BUSCLK Signal		
16MHz ASYNC		2-3
25MHz CPUCLK		1-2
33MHz CPUCLK		off

W15, 16, 17 - CPU Type			
	W15	W16	W17
486 SX	1-2	off	off
486 DX / DX2 / DX4	2-3	on	on

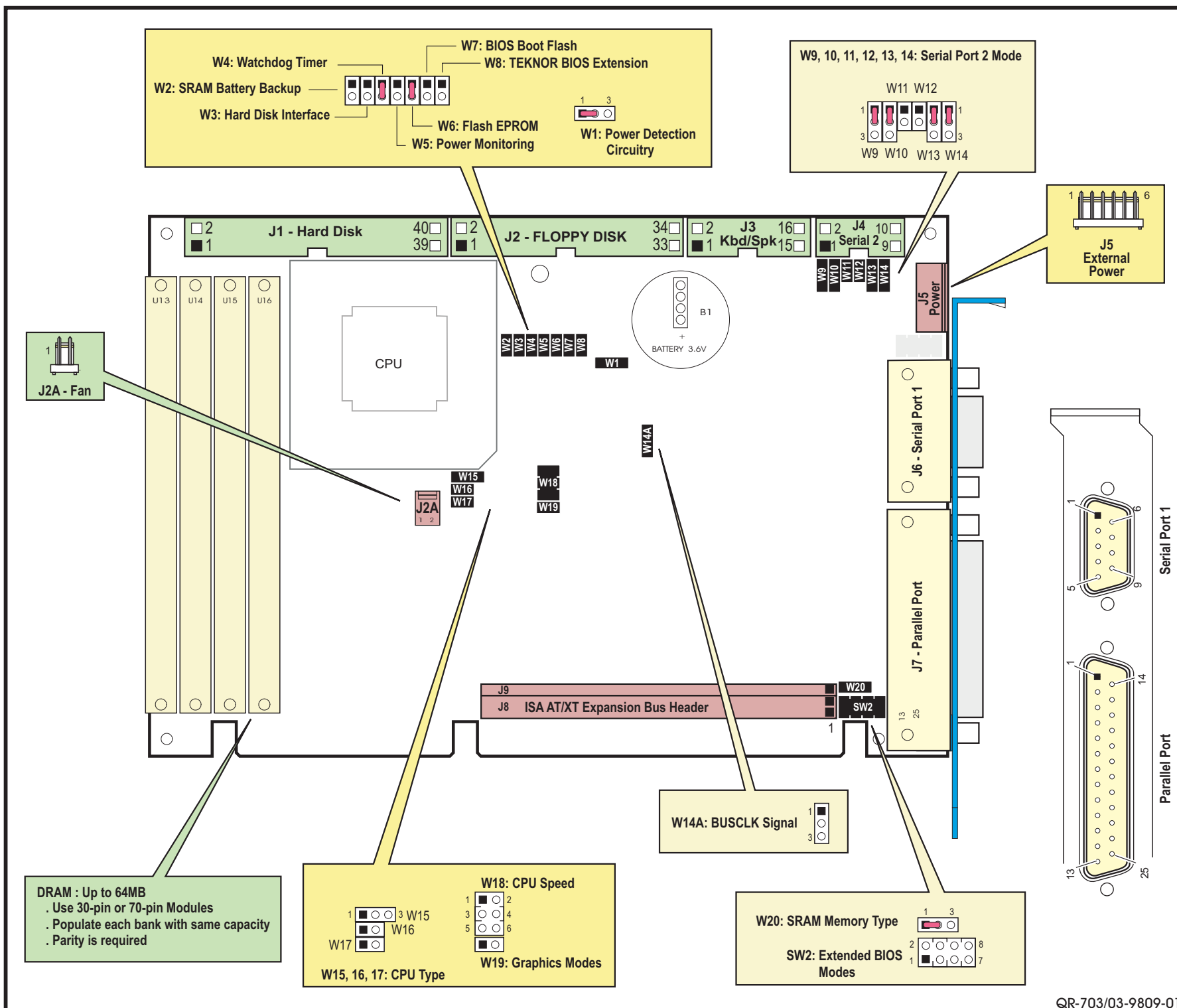
W18 - CPU Speed			
	1-2	3-4	5-6
25/50MHz	on	on	on
33/66/100MHz	off	on	on

Careful attention should be taken when installing a processor: Faulty jumper settings may damage both your processor and your board.

W19 - Graphics Modes		
Mono, EGA, VGA *		off
Color CGA Only		on

W20 - SRAM Memory Type		
32K x 8, 128K x 8		1-2
256K x 8, 512K x 8		2-3

SW2 - Extended BIOS Modes		
	on	off
1-2	Boot from Flash	Boot from Drives *
3-4	Use COM2	Use COM1 *
5-6	VT100 Mode	Standard Display Mode *
7-8	Remote Download	Normal *



QR-703/03-9809-01

CONNECTOR PINOUTS

◆ J1 - IDE Channel		* Active Low Signal			
Odd Pin Number					
1	RESET *	29	DACK *	20	N.C.
3-17	[HD7-HD0]	31	IRQ 14	28	BALE
19	GND	33 ; 35	SA1 ; SA0	30	GND
21	N.C.	37	CS0 *	32	IOCS16 *
23	IOW *	39	ACTIVE *	34	N.C.
25	IOR *	Even Pin Number		36	SA2
27	IOCHRDY *	2 ; 22-26	GND	38	CS1 *
		4-18	[HD8-HD15]	40	GND

◆ J2 - Floppy Disk		* Active Low Signal			
Odd Pin Number					
1-33	GND	14	DRIVE SEL. A *	26	TRACK 0 *
2	RPML/C	16	MOT. ENABLE B *	28	WPROTECT *
4 ; 6	N.C.	18	DIR CONTROL *	30	RDATA *
8	INDEX *	20	STEP *	32	HEAD SELECT *
10	MOT. ENABLE A *	22	WDATA *	34	DSKCHG
12	DRIVE SEL. B *	24	WENABLE *		

◆ J3 - Multi-Function (Kbd, Spkr, Reset, LED)		* Active Low Signal			
Odd Pin Number				Even Pin Number	
1	KCLK	7	SPKR OUT	15	HDACTIVE *
3	KBDATA	9	KBINH	2-4 ; 10-14	GND
5	VCC (+5V)	11	AUTO *	6-8 ; 16	VCC (+5V)
		13	PBRES *		

◆ J4 - Serial Port 2 (RS-232)		◆ J4 - Serial Port 2 (RS-485)		◆ J6 - Serial Port 1	
DCD 2		Reserved		DCD 1	
1	DSR 2	1	N.C.	1	DSR 1
2		2	N.C.	2	
RX 2	RTS 2	3	RX(+)	RX 1	RTS 1
4		4	RX(+)	2	
5	CTS 2	5		3	CTS 1
6		6		4	
7	RI 2	7	N.C.	5	
8		8	N.C.		
DTR 2		9			
9		10			
GND	N.C.				

◆ J5 - External Power		◆ J7 - Parallel Port (Standard Mode)			
Odd Pin Number		Odd Pin Number		Even Pin Number	
1	VCC(+5V)			14	AUTOFD *
2	GND	STROBE *	1	15	ERROR *
3	GND	[D0-D7]	2-9	16	INIT *
4	+12V	ACK *	10	17	SELECTIN*
5	-12V	BUSY	11	18-25	GND
6	Power Failure Detect	PE	12		
		SELECT	13		

◆ J2A - Fan	
1	+12V
2	GND

◆ J8/J9 - ISA Bus and ISA AT Extension		* Active Low Signal			
J9 - ISA AT Extension		J8 - ISA Bus			
		ROW A		ROW B	
SBHE*	1	1	IOCHK*	GND	
SA23	2	2	SD7	RESET DRV	
SA22	3	3	SD6	VCC (+5V)	
SA21	4	4	SD5	IRQ9	
SA20	5	5	SD4	-5V	
SA19	6	6	SD3	DRQ2	
SA18	7	7	SD2	-12V	
SA17	8	8	SD1	0WS*	
SD8	9	9	SD0	+12V	
SD9	10	10	IOCHRDY *	GND	
SD10	11	11	AEN	SMEMW *	
SD11	12	12	SA19	SMEMR *	
SD12	13	13	SA18	IOW *	
SD13	14	14	SA17	IOR *	
SD14	15	15	SA16	DACK3 *	
SD15	16	16	SA15	DRQ3	
MEMCS16 *	17	17	SA14	DACK1 *	
IOCS16 *	18	18	SA13	DRQ1	
IRQ10	19	19	SA12	REFRESH *	
IRQ11	20	20	SA11	SYSCLK	
IRQ12	21	21	SA10	IRQ7	
IRQ15	22	22	SA9	IRQ6	
MASTER *	23	23	SA8	IRQ5	
MEMR *	24	24	SA7	IRQ4	
MEMW *	25	25	SA6	IRQ3	
DACK5 *	26	26	SA5	DACK2 *	
DRQ5	27	27	SA4	T/C	
DACK6 *	28	28	SA3	BALE	
DRQ6	29	29	SA2	VCC (+5V)	
DACK7 *	30	30	SA1	OSC	
DRQ7	31	31	SA0	GND	

▶ I/O MAPPING			
000-00F	DMA controller 1	0F0-0FF	Math Coprocessor
020-03F	Interrupt Controller 1	1F0-1F7	Hard Disk
040-05F	Timer	201	Watchdog Timer, PDO, User
060-06F	Keyboard and Mouse	378-37A	LPT1
070-07F	Real Time Clock, NMI mask	2F8-2FF	COM2
080-09F	DMA Page Register	3F2-3F7	Floppy Disk
0A0-0BF	Interrupt Controller 2	3F8-3FF	COM1
0C0-0DF	DMA Controller 2		

TEK-AT4 TECHNICAL SPECIFICATIONS

★ **CPU TYPE & SPEED**
486SX @25MHz ; 486DX @ 33MHz ; 486DX2 @ 66MHz

★ **SYSTEM MEMORY**
DRAM: 1, 4, or 16MB using four 30-pin SIMMs
SRAM: 1MB battery-backed
Flash EPROM: 18MB onboard

★ **BUS INTERFACE**
PC/AT bus or stand-alone operation ; 100% IBM PC/AT compatible

★ **DATA PATH**
32-bit on CPU bus ; 16-bit on ISA bus

★ **I/O**
SERIAL: two serial ports, configurable as RS-232 (COM1-2) with RS-422/485 available on COM2
PARALLEL: 1 bi-directional port (LPT1)
HARD DISK: local bus IDE interface
FLOPPY DISK: interface for two 1.44 floppy drives

★ **BIOS FEATURES**
AMI BIOS in Flash EPROM ; Auto configuration and extended setup
Programmable CPU and memory wait states ; BIOS shadowing in RAM
Extension for diskless, keyboardless and videoless operations
MS-DOS and application bootup from Flash EPROM

★ **SUPERVISOR UTILITIES**
Watchdog timer ; Power Failure / low battery detection

★ **POWER SUPPLY**
VOLTAGE : +5V ±5% ; +12V ±5%

Proc. Speed	CURRENT		
	SX-25	DX-33	DX2-66
ICC typ.: 5V	1.25A	1.50A	1.80A
IPP: +12V/-12V	15/5mA	15/5mA	15/5mA
Setup	4MB DRAM ; 256KB SRAM ; 12MB Flash ; HD ; FD ; kbd ; video		

★ **OPERATING CONDITIONS**
0°C to 70°C with airflow ; R.H. : 5% to 95%

★ **ELECTRICAL / MECHANICAL**
Board dimensions : 4.7 in. x 7 in. (119.38 mm x 177.80 mm)
Conforms to IEEE P996 PC/AT bus electrical and mechanical specifications

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The Technical Reference Manual can be downloaded from the TEKNOR Web Site at www.teknor.com.

To order a hard copy of the Technical Reference Manual, contact Customer Service at (450) 437-5682.

First Level Debugging

- 1 . Remove all peripheral boards from the backplane. Only keep the SBC.
- 2 . Remove all cables from the SBC except the video cable
- 3 . Make sure the memory is properly inserted and good working



Before Powering ON the Board

- 1 . Ensure the power supply connector is connected properly (+5V, +12V, -12V)
- 2 . Make sure all cables are connected to the adequate connector