AES 80 STANDARD PRICE LIST

AUTOMATIC ELECTRONIC SYSTEMS INC

CODE	DESCRIPTION	PREREQUISITE COLUMN	PRICE
	This list gives prices for the AES-80 Microprocessor system, FOB St. Albans, Vermont. Prices are quoted for unit quantity purchase before OEM volume discounts. Prices are subject to revision without notice. The prices are a condensation of equipment available; the factory should be consulted for options and equipment needed that are not listed.		
	AES-80 MICROPROCESSOR		
80-01	Basic microprocessor including pre-wired backplane with 9 PC Board & 3 I/O connectors, and 3 timing and control PC Boards.	-	950.00
	INSTRUCTION MEMORY		
80-10-1	Bipolar ROM (256 x 12 bits) field programmable, 240 nanosecond cycle time, PC Board mounted.	80-01	350.00
	One time charge/program for above.		150.00
80-10-2	Bipolar ROM (512 x 12 bits) field programmable, 240 nanosecond cycle time, PC Board mounted.	80-01	640.00
	One time charge/program		300.00
80-10-3	Bipolar ROM (768 x 12 bits) field programmable, 240 nanosecond cycle time, PC Board mounted.	80-01	920.00
	One time charge/program		450.00

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	INSTRUCTION MEMORY		
80-10-4	Bipolar ROM (1024 x 12 bits) field programmable, 240 nanosecond cycle time, PC Board mounted.	80-01	1,210.00
	One time charge/program		600.00
80-11-1	Capacitive ROM (1024 x 12 bits), 240 nanosec cycle time.	80-01	585.00
	Masking charge/program		195.00
	Revisions of data mask (each board)		40.00
80-11-2	Capacitive ROM (2048 x 12 bits) 240 nanosec cycle time.	80-01	680.00
	Masking charge/program		350.00
	Revision of data mask (each board)	80-01	80.00
	DATA MEMORY		
80-20-1	Bipolar Random Access Memory (256 x 8 bits), 240 nanosecond cycle time.	80-01	255.00
80-20-2	Bipolar Random Access Memory (512 x 8 bits), 240 nanosecond cycle time.	80-01	445.00
80-20-3	Bipolar Random Access Memory (768 x 8 bits), 240 nanosecond cycle time.	80-01	640.00
80-20-4	Bipolar Random Access Memory (1024 x 8 bits), 240 nanosecond cycle time.	80-01	830.00
80-21-1	MOS Random Access Memory (1024 x 8 bits), 1 microsecond cycle time.	80-01	415.00

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	DATA MEMORY		
80-22-1	Core Memory Random Access memory, (1024 x 8 bits), 1 microsecond cycle time.	80-01	880.00
	DIGITAL INPUT/OUTPUT		
80-02	Digital I/O Modular System Unit including prewired backplane and differential bus interface card with capacity for 8 I/O boards.	80-01	550.00
80-02-1	16 input board with transient protection filters, optoelectronic isolation and signal conditioning.	80-02	280.00
80-02-2	16 output board including opto-electronic isolation, over-voltage protection and 200 milliamp source and sink capability.	80-02	280.00
80-02-3	8input/8output board including opto-electronic isolation on all points.	80-02	280.00
	COMMUNICATION		
80-02-4	Modem I/O with RS232 interface for control of 1 low speed modem.	80-02	200.00
80-02-5	Low speed modem card, 0-1800 baud, asynchronous.	80-02-4	595.00
	ANALOG INPUT/OUTPUT		
80-03	Multiplexed high level analog input modular system unit including prewired backplane and bus interface board with capacity for 2 A/D subsystems.	80-01	550.00
80-03-1	A/D converter with dual slope integration, 1 board per subsystem.	80-03	590.00

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CODE	DESCRIPTION	PREREQUISITE COLUMN	PRICE
	ANALOG INPUT/OUTPUT		
80-03-2	A/D timing and control, 1 board per subsystem.	80-03-1	200.00
80-03-3	A/D multiplexer, 16 channel differential, solid state, 1 or 2 boards per subsystem.	80-03-1 80-03-2	390.00
	MSU CHASSIS		
80-04	19" chassis to accept 3 modular system units with power supply.	80-01	825.00
	PROGRAM DEVELOPMENT AND CONTROL		
80-30	Console for system monitoring including display panel, power supply and maintenance logic.		2,540.00
80-31	Program development console including the above 80-30 system plus logic and a 512 x 12 bit ROM simulator.		3,990.00
80-32	Program development console including the above 80-31 system plus an internal microprocessor with self assembly capability, 2K x 12 ROM simulator, 1K x 8 data memory.		8,700.00
80-31-1	Additional ROM simulator memory 512 x 12 bits.	80-31	670.00
80-31	Additional 4K x 16 MOS memory.	80-32	2,000.00
	TELEPRINTERS		
80-40	33 ASR teletype machine, 10 char/sec, with paper tape reader and punch.	80-02 80-30	1,500.00
80-41	Teletype modification kit	-	175.00

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CODE	DESCRIPTION	PREREQUISITE COLUMN	PRICE
	TELEPRINTERS		
8Ø-42	33 Teletype and/or high speed paper tape reader interface.	8Ø-4Ø	225.00
8Ø-43	RS 232 C teleprinter and/or high speed paper tape reader interface.	-	225.00
8Ø-44	High speed paper tape reader assembly, 75 char. per second.	-	495.00

DISCOUNT TERMS

AES products not subject to discount are normally indicated on AES' price list and include: spare parts, software and peripheral equipment not manufactured by AES.

The price of each discountable product type, under these terms, is computed on a non-accumulative basis as purchase orders are received. The placement of additional orders does not reduce the price of previously received orders.

Deliveries will only be scheduled upon receipt of a duly executed Purchase Order or release from Buyer. Any addition to a given configuration shall be on a separate order and may result in a delay in the scheduled delivery date.

DISCOUNT SCHEDULE

NO. of Units		Discount %
1		10
2		15
3 - 5		19
6 - 9		23
10 - 14		26
15 - 19		29
20 - 29		31
30 - 49		34
50 - 74		36
75 - 99		37
100 - 149		38
150 - 199		39
200 - 499		40
500 - 999		45
1000 OR +		50

DISCOUNT FORFEITURE

Discounts under this agreement will be forfeited by the Buyer with respect to equipment for which payment is not received by AES within thirty (30) days after delivery.