



PCI 9050RDK

Design Note Rev. 1.0
June 2001

A. Product Status

The scope of this document encompasses the PCI 9050RDK Rapid Development Kit

1. PCI 9050RDK Serial EEPROM Manufacture Setting

Serial EEPROM Offset	Serial EEPROM Value	Description	Register Affected
00	905010B5	Device ID, Vendor ID	PCIIDR
04	06800001	Class Code	PCICCR
08	905010B5	Subsystem ID, Subsystem Vendor ID	
0c	00000100	Maximum Latency, Minimum Grant, Int Pin, Int Rounting	
10	FFF00000	Local Address Space 0 Range	LAS0RR
14	FFFFFFF1	Local Address Space 1 Range	LAS1RR
18	FFF00000	Local Address Space 2 Range	LAS2RR
1c	FFF00000	Local Address Space 3 Range	LAS3RR
20	00000000	Expansion ROM Range	EROMRR
24	02000001	Local Address Space 0 Base Address (Re-Map)	LAS0BA
28	03000001	Local Address Space 1 Base Address (Re-Map)	LAS1BA
2c	01000001	Local Address Space 2 Base Address (Re-Map)	LAS2BA
30	04000001	Local Address Space 3 Base Address (Re-Map)	LAS3BA
34	00000000	Expansion ROM Base Address (Re-Map)	EROMBA
38	40000022	Local Address Space 0 Bus Region Descriptors	LAS0BRD
3c	40000022	Local Address Space 1 Bus Region Descriptors	LAS1BRD
40	00800005	Local Address Space 2 Bus Region Descriptors	LAS2BRD
44	542138E9	Local Address Space 3 Bus Region Descriptors	LAS3BRD
48	00000000	Expansion ROM Bus Region Descriptors	EROMBRD
4c	02080001	Chip Select 0 Base	CS0BASE
50	03000009	Chip Select 1 Base	CS1BASE
54	01010001	Chip Select 2 Base	CS2BASE
58	04080001	Chip Select 3 Base	CA3BASE
5c	00000041	Interrupt Control/Status	INTCSR
60	08784B46	User I/O, EEPROM, Init Control	CNTRL

2. PLXMon (DOS) and PLXMon 95 Source Code Is Not Provided

Analysis:

The PLXMon's User Guide Page 5-3 indicates that "Contact PLX for information about obtaining PLXMon source code". Since the PLXMon (DOS) and PLXMon 95 are no longer supported by PLX, the source code will not be provided any more.

Workaround:

Please use PLX SDK-LITE v3.2 or SDK-PRO v3.2 (or later versions). Both SDKs contain Windows 98/NT/200 drivers with source code and the newest version of the PLXMon to support PCI 9050RDK and PCI 9050 chip. The PRO version also includes the API source code for PCI 9050.

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